

*Fifth International Conference on Genetic and Evolutionary Computing
(ICGEC-2011)*

August 29 – September 1, 2011, Kinmen City, Taiwan

Call for Paper

ICGEC-2011-IS16

Invited Session on Innovative Circuits and Systems

Session Organizer: Prof. Hung-Yu Wang

We are organizing an invited session on Innovative Circuits and Systems for ICGEC-2011, which will be held in Kinmen City, Taiwan on August 29 – September 1, 2011. This session deals with new technologies for innovative designs of circuit, system and their applications. Topics of interest include, but are not limited to:

1. Analog/Mixed-Signal and RF Circuits
2. Advanced VLSI Designs
3. Circuits for Advanced Memory
4. Sensors, Actuators and Signal Conditioning Circuits
6. High-Power and High-Voltage Electronics
7. FPGA Applications for Signal Processing
8. System-on-Chip (SoC), System-in-Package (SiP) and IP for Signal Processing
9. High-Performance Circuits and Systems
10. Others about Innovative Circuit and System.

Submissions:

Papers are invited from prospective authors with interest on the related areas. Each paper should follow the IEEE format with title, author's names, affiliation, email addresses, an up to 150-words abstract, and a two-column body with 4 single-spaced pages and with font size at 10 pts. All papers must be submitted electronically in PDF format only and be mailed to: Prof. Hung-Yu Wang at hywang@cc.kuas.edu.tw

Important Dates:

Deadline for paper submission: April 30, 2011

Notification of acceptance: May 31, 2011

Deadline for camera-ready manuscript submission: June 30, 2011

For more information please contact:

Prof. Hung-Yu Wang

Department of Electronic Engineering

National Kaohsiung University of Applied Sciences

415 Chien-Kung Road, Kaohsiung 807, Taiwan, R.O.C.

Tel: +886-7-3814526 ext.5640

Fax: +886-7-3811182

E-mail: hywang@cc.kuas.edu.tw