Fourth International Conference on Genetic and Evolutionary Computing (ICGEC-2010)

December 13-15, 2010, Shenzhen, China

Call for Paper

Invited Session on Innovative Embedded Computing Session Organizer: Prof. Yuan-Long Jeang

We are organizing an invited session on Innovative Embedded Computing for ICGEC-2010, which will be held in Shenzhen, China on December 13-15, 2010. This session deals with new technologies for embedded system and application. Topics of interest include, but are not limited to:

- 1. Embedded Software
- 2. Instruction Compression/Decompression for Embedded Processor
- 3. Data/Control Compression/Decompression for Embedded Processor
- 4. Embedded In-circuit Emulator
- 5. Computer-Aided Design for Embedded Network-on-chip
- 6. Computer Arithmetic Embedded Processor
- 7. FPGA Applications for Embedded System
- 8. Embedded Processor Applications (including Robot Design, Multimedia Designs, etc.)
- 9. Applications for Network-on-chip
- 10. Others about embedded System.

Submissions:

Papers are invited from prospective authors with interest on the related areas. Each paper should follow the IEEE format with title, author's names, affiliation, email addresses, an up to 150-words abstract, and a two-column body with 4 single-spaced pages and with font size at 10 pts. All papers must be submitted electronically in PDF format only and be mailed to: Prof. Yuan-Long Jeang at yljeang@mail.ksu.edu.tw

Important Dates:

Deadline for paper submission: May 31st, 2010

Notification of acceptance: July 31st, 2010

Deadline for camera-ready manuscript submission: August 31st, 2010

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