

*The First International Conference on Robot, Vision and Signal Processing –  
(RVSP-2011)*

*November 21-23, 2011, Kaohsiung, Taiwan*

**Call for Paper**

**Invited Session on Software and Hardware Designs of  
Multimedia Networking Embedded Systems**

**Session Organizer: Prof. Mong-Fong Horng**

We are organizing an invited session on Software and Hardware Designs of Multimedia Networking Embedded Systems for RVSP-2011, which will be held in Kaohsiung, Taiwan on November 21-23, 2011. This session deals with new technologies for embedded system and application. Topics of interest include, but are not limited to:

1. Software and hardware implementations/designs for multimedia network applications
2. Software and hardware co-designs/co-simulations/co-verifications for multimedia network applications
3. VLSI/SoC architectures and silicon intellectual properties (SIP) for multimedia and network systems
4. Embedded In-circuit Emulator
4. Signal processing approaches for multimedia communications and networks
5. Wireless sensor/ad-hoc networks for multimedia communications and networks
6. IPTV hardware and software designs
7. Image processing/compression algorithms
8. Embedded systems for digital home applications

**Submissions:**

Papers are invited from prospective authors with interest on the related areas. Each paper should follow the IEEE format with title, author's names, affiliation, email addresses, an up to 150-words abstract, and a two-column body with 4 single-spaced pages and with font size at 10 pts. All papers must be submitted electronically in PDF format only and be mailed to the session organizer.

**Important Dates:**

Deadline for paper submission: May 5<sup>th</sup>, 2011

Notification of acceptance: July 1<sup>st</sup>, 2011

Deadline for camera-ready manuscript submission: August 1<sup>st</sup>, 2011

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