FIFTH INTERNATIONAL CONFERENCE ON INFORMATION ASSURANCE AND SECURITY
(IAS-2009)

August 18-20 2009
Xi'an, China

CALL FOR PAPERS

SPECIAL SESSION: SYSTEM-ON-CHIP FOR SIGNAL PROCESSING

This session will have discussions of the system-on-chip (SoC) design with the expert opinions from various fields in signal processing, image processing, communication security and so on. The SoC has changed the system-level design concept and application significantly. This change is not just the exploration of design methodology. The SoC has made the real-time system to be realized into a single chip, in "light, thin, small". This session is expected to discuss the SoC design techniques for real-time signal processing applications. This session will address the following issues (not limited):

- The design of silicon intellectual property
- Hardware/software co-design
- SoC prototyping implementation
- SoC design for image processing system
- SoC design for communication system

Full Paper Due Date: Mar. 01, 2009
Notification: Apr. 01, 2009
Camera-ready Paper Deadline: May 01, 2009
The Conference Proceedings will be published by IEEE Press.

SUBMISSION OF PAPERS

Papers are invited from prospective authors with interests in this particular special session and related areas of application. All contributions should be original and not published elsewhere or intended to be published during the review period. Contributions from more applied related fields in industry and commerce are very welcome. Each paper should follow the IEEE paper format (DOC, LaTex Formatting Macros, PDF, PS) with title, authors' names, affiliations and email addresses, an up to 150-words abstract, and a two-column body with 4 single-spaced pages and with font size at 10 pts. All papers are to be submitted electronically in PDF format to the special session organizer. Details of the required paper format are published at the official IAS09 website (http://www.ias09.org/ and http://bit.kuas.edu.tw/~ias09/).

For further information, please contact:

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